

MARCO MARINI

Senior Consulting Engineer

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Date of birth: 23.04.1969

EXPERTISE

ASIC Verification: Block level: code coverage, formal, systemverilog (beginner), specman (beginner), IP (communication, dma, memory interface, automotive, printer, bus)
System-level: Soc, advanced testbenches developed from scratch, co-verification [hw/sw], leadership

Digital Design: RTL design [VHDL, Verilog]: 2 IP developed, top-level SoC integration
Front2Back: Synthesis, Pre/Post Layout timing closure, Scan insertion, DFT, UPF
Layout: as user

Firmware: Assembler (deep expertise), C

Software: C, VisualC++

ARM/Embedded: Classical uproc [arm7, arm9, Cortex], AMBA [AHB, APB], MMU/MPU, Cache

EDA tools: Cadence [ncsim], Synopsys [Design Compiler, Formality], Xilinx [Ise]

Electronic Design: Radio system controller card, test card

System Integration: PDH Radio System, HW/SW integration

FPGA: as user for ASIC prototyping purposes

Documentation: Test and Design specifications

Linux/Unix: Revision control [Synchronicity, CVS], make, scripting (shell, Tcl, Perl)

Web 2.0: LAMP [Linux, Apache, MySQL, PHP], CSS, CMS (Joomla)

PROFESSIONAL EXPERIENCE

Pegasus Micro-Design S.r.l. - Arcore (MB), Italy – *Electronic services*
Senior consulting engineer (from 2009 to february 2014)

ASIC digital design and verification @ ST Microelectronics (Computer and Peripherals, Communications, Automotive, Energy), @ Alcatel (100G optical transmission), @ Movidius (3D mobile application SoC)

Accent S.p.A. - Vimercate (Milano), Italy - *Electronic services*
Consulting engineer (from 2000 to 2009)

ASIC digital design and verification @ ST Microelectronics (APG, Industrial, Auto & Audio, Printers)

Siemens ICN S.p.A - Transmissions BU - Cassina de' Pecchi (Milano), Italy
Junior designer and System integrator (from 1997 to 2000)

PDH radio system:

- > Controller (intel80x86) card design and integration
- > Self-test firmware (assembler) design and integration, Test cards design
- > Radio network management verification and integration (with the controller)
- > Hardware / Software interfaces specifications, Labs, Sales support, Supply chain, Production support, Product qualification, Design suppliers management

EDUCATION

- > **Qualification to practise engineer profession** (1998)
- > **MSEE, Master of Science Electronic Engineering** (Telecommunication)
Università degli Studi di Perugia – 1997 - Mark: 102/110
Thesis title: *Design of an examination list management system through RDBMS techniques in web environment.*
- > **Internship** (1996)
ELES Equipment S.r.l. - Todi (PG) (1996, 6 months)
VisualC++ software application design (Microsoft Foundation Classes user interface) to parse, get and view RTF (Rich Text Format) reports.
- > **General Certificate of Education** (1988) @ Liceo Scientifico G. Alessi (Perugia) - mark: 60/60

FOREIGN LANGUAGES

English: autonomous user

SKILLS

- > team work
- > focusing on customer needs

CAREER OBJECTIVES

I wish to work on state-of-the-art applications and advanced companies, within a multi-disciplinary team, increasing more and more my competence and responsibility in education, design and verification

TRANSFERS AVAILABILITY

Availability to temporary transfers to Italy and foreign countries.

ADDITIONAL INFORMATION

- > State High School substitute teacher (Electronics), 4 months in 2018
- > Foudner and President of no profit association *Cibo in salute* (<http://ciboinsalute.it>) since 2015 about health, food, agriculture, lifestyle.
- > LAMP, CMS (joomla) web portals (design, management, counseling) on Sustainable mobility, e-collaboration, investigative journalism on health (vaccination, iatrogenicity, epigenetics, microbiom)
- > Accounting Analysis (3 months)
- > Free Tourist Guide (not official)
- > Italian army military service: lieutenant (2 years)
- > Catholic Church voluntary (3 years)
- > Soccer trainer (2 years)
- > Seed savers, Farming and gardening (6 years)

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Perugia, 29.05.2018